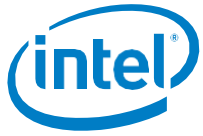


Intel[®] Ethernet Controller I225

Specification Update, Revision 1.0

Intel Corporation

March 2020



Revision History

Revision	Date	Comments
1.0	March 27 th , 2020	Initial Release

1. Introduction

This document applies to the Intel® Ethernet Controller I225. It covers product level details, and an erratum related to IEEE behavior in 2.5Gbps mode.

1.1 Product Code and Device Identification

Product Codes: I225.

The following tables and drawings describe the various identifying markings on each device package:

Table 1-1 I225 Part Identification Summary

Part #	Version	Intel MM #	Spec Code	Type	Network Driver Branding String
I225-LM	V1	999JW1	SLN9B	T&R	Intel(R) Ethernet Controller I225-LM
I225-LM	V1	999JW0	SLN9A	Tray	Intel(R) Ethernet Controller I225-LM
I225-V	V1	999JW7	SLN9D	T&R	Intel(R) Ethernet Controller I225-V
I225-V	V1	999JW5	SLN9C	Tray	Intel(R) Ethernet Controller I225-V
I225-LM	V2	99A1TL	SLNJW	T&R	Intel(R) Ethernet Controller (2) I225-LM
I225-LM	V2	99A1T7	SLNJV	Tray	Intel(R) Ethernet Controller (2) I225-LM
I225-V	V2	99A1VF	SLNJV	T&R	Intel(R) Ethernet Controller (2) I225-V
I225-V	V2	99A1V1	SLNJX	Tray	Intel(R) Ethernet Controller (2) I225-V

Figure 1-1 shows a snapshot of the I225 device:



Figure 1-1 Snapshot of I225

1.2 Marking Diagrams

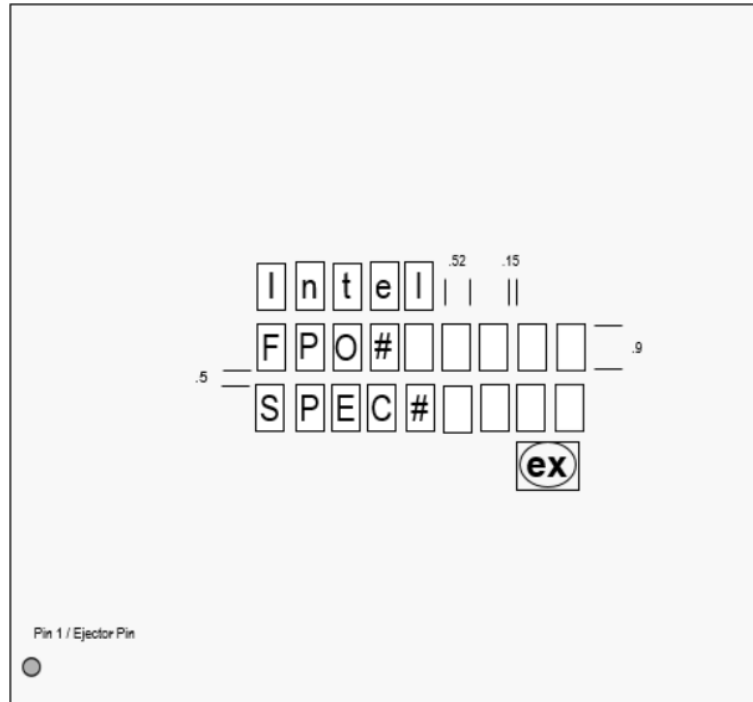



Figure 1-2 I225 Marking Pattern

Notes:

- LINE1: Logo - Intel Swirl Logo
- LINE2: FPO# - Assembly lot marking, 8 characters
- LINE3: Spec#
- LINE4: e# - Pb-free symbol
- LINES OF TEXT ARE CENTERED ON THE PACKAGE AND LEFT JUSTIFIED
- Zero should have a slash "/" in the middle.

 • Pb- free symbols



2. Erratum

2.1 I225 v1 Minimum Supported Receive Inter-Packet Gap (IPG) is 8 bytes instead of 5 bytes

Problem Summary:

The minimum received inter packet gap (IPG) in the GMII interface between the PHY and the MAC is 8 bytes. This minimum gap is specified through 1Gbps operation. For a controller to operate at 2.5Gbps, the IEEE standard requires that the MAC be able to tolerate a minimum IPG of 5 bytes.

The I225 v1 silicon supports an IPG down to 8 bytes, but not down to 5 bytes in 2.5Gbps operation.

Implications:

Packets received by the I225 v1 product link partner (the switch, router, or back-to-back controller it is connected to) that are received with a smaller IPG than 8 bytes on the MAC/PHY interface are dropped. When this occurs, the network connectivity is reduced from 2.5Gbps to below 1Gbps operation. In some scenarios with certain link partners, the operation can be as low as 1-10Mbps.

Workaround:

The impact of this IPG issue can be mitigated when connected through a switch/router by setting the link speed in the driver (Windows or Linux) to 1Gbps operation to enable a 1Gbps line rate.

Also, the latest Windows drivers and I225 firmware available for download (driver version 1.0.0.30 or later NVM version 1.38 or later) can detect when the IPG packet drops occur and will automatically reset the link speed to 1Gbps. Please see downloads here:

<https://downloadcenter.intel.com/product/184686/intel-ethernet-controller-i225-series>

Known Compatible 2.5Gbps Link Partners:

Below is a list of link partners with which Intel has tested I225 v1 to enable 2.5Gbps operation.

Brand*	2.5Gbps Router/Switch or Controller Model(s)
Aruba	2930m, 3810m (Switches)
Asus	Rapture GT-AX11000 (Router)
Buffalo	BS-MP2012, BS-MP2008 (Switches)
Cisco	Catalyst 3850-24XU & 9300-24X UPOE (Switches)
D-Link	DMS-1100-10TP (Switches)
Huawei	S6720-32C-SI (Switches)
Netgear	Nighthawk AX12/RAX120 (Router)
Trendnet	TEG-7080ES, TEG-30102WS (Switches)

Status:

Intel has resolved this issue with I225 v2, which is now in production.



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